L Number	Hits		DB	Time stamp
1	668	semiconductor and substrate and (trench or groove) and (thermal adj oxide) and ((silicon or polysilicon or Si) near4 fill\$)	USPAT	2003/12/11 13:57
2	224	and (vapor adj deposition)		0007/10/11
	224	groove) and (thermal adj oxide) and	US-PGPUB	2003/12/11 13:57
		((silicon or polysilicon or Si) near4 fill\$) and (vapor adj deposition)		
4	0	semiconductor and substrate and (trench or groove) and (thermal adj oxide) and ((silicon or polysilicon or Si) near4 fill\$)	EPO	2003/12/11 13:58
	0	and (vapor adj deposition)		
3		semiconductor and substrate and (trench or groove) and (thermal adj oxide) and ((silicon or polysilicon or Si) near4 fill\$) and (vapor adj deposition)	JPO	2003/12/11 13:58
5	5	semiconductor and substrate and (trench or groove) and (thermal adj oxide) and ((silicon or polysilicon or Si) near4 fill\$) and (vapor adj deposition)	DERWENT	2003/12/11 13:58
6	2	semiconductor and substrate and (trench or groove) and (thermal adj oxide) and ((silicon or polysilicon or Si) near4 fill\$) and (vapor adj deposition)	IBM_TDB	2003/12/11 13:59

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